Name:	200. 717	
Reg. No.	:	
END	TERM EXAMINATION - No	
	2000/I	vember/December-
	ZUZZ/January-Zi	023
	SEMESTER - V (B. Tech. CSE, 2019, 2020 at	
Subject	Code: CS2007/21CS2007	Duration: 3 hours
Subject:	Computer Architecture	Max. Marks: 100
Instruction	ons	
	Il Questions are compulsory	
• T qr m	the Question paper consists of 2 so uestions of 2 marks each. Part B o arks each.	consists of 5 questions of 10
	here is no overall choice. Only sternal choice.	rart B question include
	PART – A	
	(2 * 10 = 20 Mari	ks)
1 Give e	xample of evolution of compute	er systems
Civa	n example of computer types a	nd functional units.
- n	a diagram of arithmetic logical	unit.
	e I maint orithmetic With a	n example.
A	the components of control t	unit design?
and the second second second second	Ale avamples of semiconduct	ctor memory:
		nocessing.
10. What	t is data transfer techniques?	DMA Page 1/
		54-10 S 3 V

PART - B (16 * 5 = 80 Marks) 11. a) Explain with examples, difference between RISC and CISC (8+8)OR b) With examples, explain instruction set sequencing. (8+8)12.a) What are the concepts of pipelining? OR b) How a complete instruction is executed? 13.a) How microprogrammed control works? Explain with examples. (8+8)OR b) How hardwired control works? Explain with examples (8+8) 14. a) Show working of optical magnetic tape. OR b) How does virtual memory work? Also give examples. 15.a) Give overview and challenges of direct memory access. (8+8)

OR

b) Give detailed explanation of I/O processor.